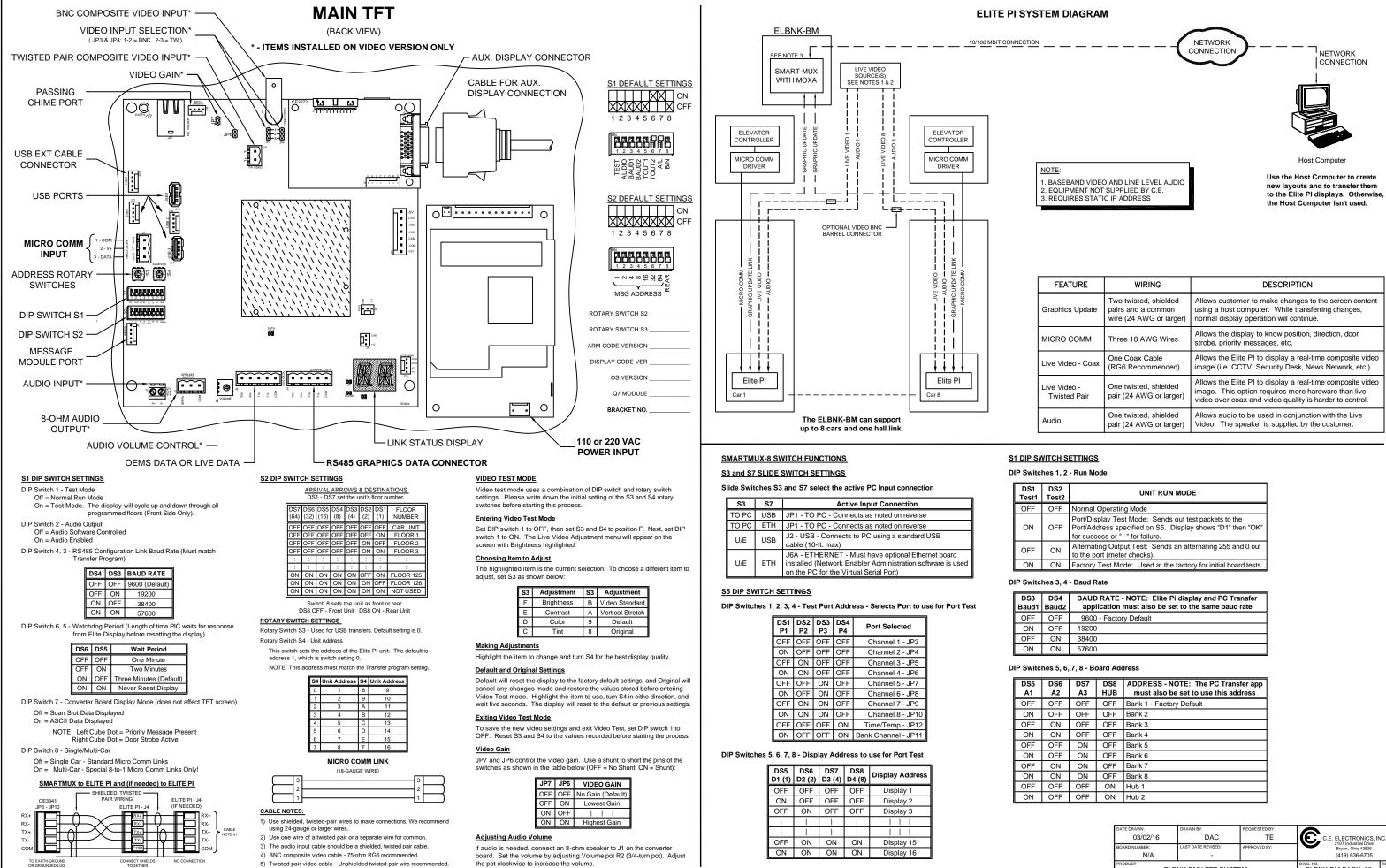


DS1 P1	DS2 P2	DS3 P3	DS4 P4	Port to Test (JPn)	Display (Cn)
OFF	OFF	OFF	OFF	Channel 1 - JP3	C1
ON	OFF	OFF	OFF	Channel 2 - JP4	C2
OFF	ON	OFF	OFF	Channel 3 - JP5	C3
ON	ON	OFF	OFF	Channel 4 - JP6	C4
OFF	OFF	ON	OFF	Channel 5 - JP7	C5
ON	OFF	ON	OFF	Channel 6 - JP8	C6
OFF	ON	ON	OFF	Channel 7 - JP9	C7
ON	ON	ON	OFF	Channel 8 - JP10	C8

DATE DRAWN: 03/02/16	DRAWN BY: DAC	REQUESTED BY: TE	C.E. ELECTRONICS, INC.	
BOARD NUMBER: N/A	LAST DATE REVISED:	APPROVED BY:	Bryan, Ohio 43506 (419) 636-6705	
ELBNK with MOXA and Video Couplers			DWG. NO. ELBNK-BMX_02	REV:



Normal Operating Mode Port/Display Test Mode: Sends out test packets to the
Port/Display Test Mode: Sends out test packets to the
Port/Address specified on S5. Display shows "D1" then "OK" for success or "" for failure.
Alternating Output Test: Sends an alternating 255 and 0 out to the port (meter checks).
Factory Test Mode: Used at the factory for initial board tests.

1	DS4 Baud2	BAUD RATE - NOTE: Elite Pi display and PC Transfer application must also be set to the same baud rate
	OFF	9600 - Factory Default
	OFF	19200
	ON	38400
	ON	57600

DS6 A2	DS7 A3	DS8 HUB	ADDRESS - NOTE: The PC Transfer app must also be set to use this address
OFF	OFF	OFF	Bank 1 - Factory Default
OFF	OFF	OFF	Bank 2
ON	OFF	OFF	Bank 3
ON	OFF	OFF	Bank 4
OFF	ON	OFF	Bank 5
OFF	ON	OFF	Bank 6
ON	ON	OFF	Bank 7
ON	ON	OFF	Bank 8
OFF	OFF	ON	Hub 1
OFF	OFF	ON	Hub 2

DATE DRAWN: 03/02/16	DRAWN BY: DAC	REQUESTED BY: TE	C.E. ELECTRONICS, IN 2107 Industrial Drive		
BOARD NUMBER: N/A	LAST DATE REVISED:	APPROVED BY:	Bryan, Ohio 43506 (419) 636-6705		
PRODUCT	NK-BMX TFT SYSTE	M	ELBNK-BM BACK_02	REV:	