# OV220-X

JOB# _____



PASSING CHIME CONNECTOR

PROCESSOR

CE2600

- V+
- COM
- L2
- L1

OTIS SERIAL CONTROLLER CONNECTION

TST
1
2
4
8
16
32
LAN

EEPROM
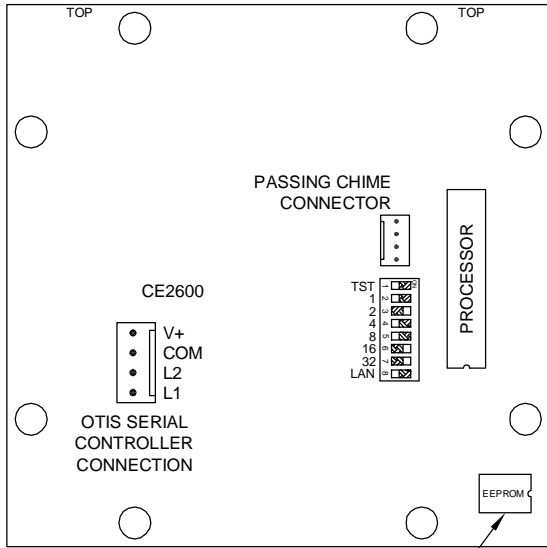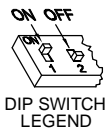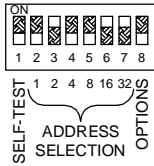
EEPROM IS USED TO DISPLAY DIFFERENT CHARACTERS THAN ARE RECEIVED. USE *"OCDL.EXE"* PROGRAM TO CREATE EEPROM DATA.
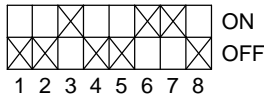
TOP   TOP



THE ARROWS APPEAR IN THE TENS DIGIT WHEN NO CHARACTER IS BEING DISPLAYED THERE. IF ARROWS ARE NOT REQUIRED, SET THE DIP SWITCHES TO AN ADDRESS LOWER THAN FOUR.

## DIP SWITCH DETAIL



ON
1 2 3 4 5 6 7 8

SELF-TEST

1 2 4 8 16 32
ADDRESS SELECTION

OPTIONS

ON OFF

DIP SWITCH LEGEND

## DEFAULT SETTINGS



ON
OFF
1 2 3 4 5 6 7 8

CODE VERSION _____

BOARD VERSION CE2600 ___

## DATA BITS

1 2 3 4

NOT USED ———————— DOWN ARROW
PASSING CHIME (LPT) ———————— UP ARROW

UP ARROW - - - - PASSING CHIME (LPT)
DOWN ARROW - - - - NOT USED
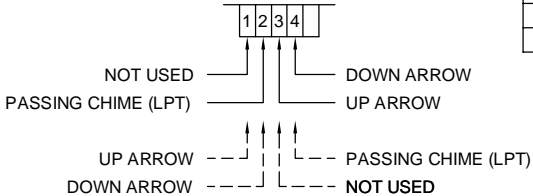
——————— = DIP SWITCH 8 OFF
- - - - - - - = DIP SWITCH 8 ON

## CHART TO SELECT ADDRESS WITH DIP SWITCH

| VALUE | 32 | 16 | 8 | 4 | 2 | 1 | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **DIP SWITCH #** | **7** | **6** | **5** | **4** | **3** | **2** | **ADDRESS** | | | | | | | |
| | 0 | 0 | 0 | 0 | 0 | 0 | INVALID | 1 | 0 | 0 | 0 | 0 | 0 | ADDRESS #32 |
| | 0 | 0 | 0 | 0 | 0 | 0 | INVALID | 1 | 0 | 0 | 0 | 0 | 1 | ADDRESS #33 |
| | 0 | 0 | 0 | 0 | 0 | 0 | INVALID | 1 | 0 | 0 | 0 | 1 | 0 | ADDRESS #34 |
| | 0 | 0 | 0 | 0 | 0 | 0 | INVALID | 1 | 0 | 0 | 0 | 1 | 1 | ADDRESS #35 |
| | 0 | 0 | 0 | 1 | 0 | 0 | ADDRESS #4 | 1 | 0 | 0 | 1 | 0 | 0 | ADDRESS #36 |
| | 0 | 0 | 0 | 1 | 0 | 1 | ADDRESS #5 | 1 | 0 | 0 | 1 | 0 | 1 | ADDRESS #37 |
| | 0 | 0 | 0 | 1 | 1 | 0 | ADDRESS #6 | 1 | 0 | 0 | 1 | 1 | 0 | ADDRESS #38 |
| | 0 | 0 | 0 | 1 | 1 | 1 | ADDRESS #7 | 1 | 0 | 0 | 1 | 1 | 1 | ADDRESS #39 |
| | 0 | 0 | 1 | 0 | 0 | 0 | ADDRESS #8 | 1 | 0 | 1 | 0 | 0 | 0 | ADDRESS #40 |
| | 0 | 0 | 1 | 0 | 0 | 1 | ADDRESS #9 | 1 | 0 | 1 | 0 | 0 | 1 | ADDRESS #41 |
| | 0 | 0 | 1 | 0 | 1 | 0 | ADDRESS #10 | 1 | 0 | 1 | 0 | 1 | 0 | ADDRESS #42 |
| | 0 | 0 | 1 | 0 | 1 | 1 | ADDRESS #11 | 1 | 0 | 1 | 0 | 1 | 1 | ADDRESS #43 |
| | 0 | 0 | 1 | 1 | 0 | 0 | ADDRESS #12 | 1 | 0 | 1 | 1 | 0 | 0 | ADDRESS #44 |
| | 0 | 0 | 1 | 1 | 0 | 1 | ADDRESS #13 | 1 | 0 | 1 | 1 | 0 | 1 | ADDRESS #45 |
| | 0 | 0 | 1 | 1 | 1 | 0 | ADDRESS #14 | 1 | 0 | 1 | 1 | 1 | 0 | ADDRESS #46 |
| | 0 | 0 | 1 | 1 | 1 | 1 | ADDRESS #15 | 1 | 0 | 1 | 1 | 1 | 1 | ADDRESS #47 |
| | 0 | 1 | 0 | 0 | 0 | 0 | ADDRESS #16 | 1 | 1 | 0 | 0 | 0 | 0 | ADDRESS #48 |
| | 0 | 1 | 0 | 0 | 0 | 1 | ADDRESS #17 | 1 | 1 | 0 | 0 | 0 | 1 | ADDRESS #49 |
| | 0 | 1 | 0 | 0 | 1 | 0 | ADDRESS #18 | 1 | 1 | 0 | 0 | 1 | 0 | ADDRESS #50 |
| | 0 | 1 | 0 | 0 | 1 | 1 | ADDRESS #19 | 1 | 1 | 0 | 0 | 1 | 1 | ADDRESS #51 |
| | 0 | 1 | 0 | 1 | 0 | 0 | ADDRESS #20 | 1 | 1 | 0 | 1 | 0 | 0 | ADDRESS #52 |
| | 0 | 1 | 0 | 1 | 0 | 1 | ADDRESS #21 | 1 | 1 | 0 | 1 | 0 | 1 | ADDRESS #53 |
| | 0 | 1 | 0 | 1 | 1 | 0 | ADDRESS #22 | 1 | 1 | 0 | 1 | 1 | 0 | ADDRESS #54 |
| | 0 | 1 | 0 | 1 | 1 | 1 | ADDRESS #23 | 1 | 1 | 0 | 1 | 1 | 1 | ADDRESS #55 |
| | 0 | 1 | 1 | 0 | 0 | 0 | ADDRESS #24 | 1 | 1 | 1 | 0 | 0 | 0 | ADDRESS #56 |
| | 0 | 1 | 1 | 0 | 0 | 1 | ADDRESS #25 | 1 | 1 | 1 | 0 | 0 | 1 | ADDRESS #57 |
| | 0 | 1 | 1 | 0 | 1 | 0 | ADDRESS #26 | 1 | 1 | 1 | 0 | 1 | 0 | ADDRESS #58 |
| | 0 | 1 | 1 | 0 | 1 | 1 | ADDRESS #27 | 1 | 1 | 1 | 0 | 1 | 1 | ADDRESS #59 |
| | 0 | 1 | 1 | 1 | 0 | 0 | ADDRESS #28 | 1 | 1 | 1 | 1 | 0 | 0 | ADDRESS #60 |
| | 0 | 1 | 1 | 1 | 0 | 1 | ADDRESS #29 | 1 | 1 | 1 | 1 | 0 | 1 | ADDRESS #61 |
| | 0 | 1 | 1 | 1 | 1 | 0 | ADDRESS #30 | 1 | 1 | 1 | 1 | 1 | 0 | ADDRESS #62 |
| | 0 | 1 | 1 | 1 | 1 | 1 | ADDRESS #31 | 1 | 1 | 1 | 1 | 1 | 1 | ADDRESS #63 |

| DIP1 | DIP8 | FUNCTION |
|---|---|---|
| 1 | X | SELF-TEST MODE |
| 0 | 0 | ARROW USES BIT 3 (UP) AND BIT 4 (DOWN). CHIME USES BIT 2. |
| 0 | 1 | ARROW USES BIT 1 (UP) AND BIT 2 (DOWN). CHIME USES BIT 4. |

The following Otis data must be furnished at the specified addresses for the Otis Serial Position Indicator to work properly. The address is selected by setting DIP switches 2-7 as shown on the back of this page. The board reads the address determined by the DIP switch setting. For example, if the DIP switch is set to address 50, the board will read the bits at address 50.

At DIP switch address—selected by the DIP switch on the unit (Default 50):

DIP switch 1 puts the unit in self-test mode.

DIP switch 1 and DIP switch 8 *OFF*:

Bit 1—Not Used
Bit 2—Passing Chime (LPT)
Bit 3—Up Arrow
Bit 4—Down Arrow

DIP switch 1 *OFF* and DIP switch 8 *ON*:

Bit 1—Up Arrow
Bit 2—Down Arrow
Bit 3—Not Used
Bit 4—Passing Chime (LPT)

Two position indicators can be used at the same address if the passing chime—LPT is not attached or programmed. Just set DIP switch 8 to opposite values on the two units.

The Otis SPI display can substitute different floor characters for the ones the controller sends, and it can switch between floor characters and ASCII characters for a message. To display alternative floor characters or messages, an EEPROM created using the *OCDL.EXE* program must be installed in the unit. Message characters alternate with floor characters every second.

Messages use the two addresses following the address set by the DIP switch.

DIP switch address +1 (Default 51):                              Priority

| | | |
|---|---|---|
| Bit 1—FSL | Fire Service Lamp | 1 |
| Bit 2—Available | Message Two | 2 |
| Bit 3—FNDG/RNDG | Front/Rear Nudging | 3 |
| Bit 4—ISCL | Independent Service | 4 |

DIP switch address +2 (Default 52):

| | | |
|---|---|---|
| Bit 1—OLS | Overload Lamp | 5 |
| Bit 2—Available | Message Six | 6 |
| Bit 3—Available | Message Seven | 7 |
| Bit 4—Available | Message Eight | 8 |

NOTE: If messages are not programmed in the EEPROM, these two addresses are available for other devices, such as other position indicators.