

# OSRLD-5

JOB# \_\_\_\_\_

CODE VERSION \_\_\_\_\_

BOARD VERSION CE2641 \_\_\_\_\_

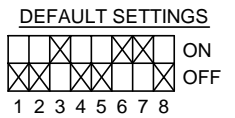
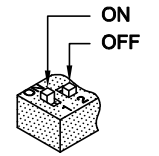
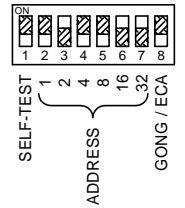
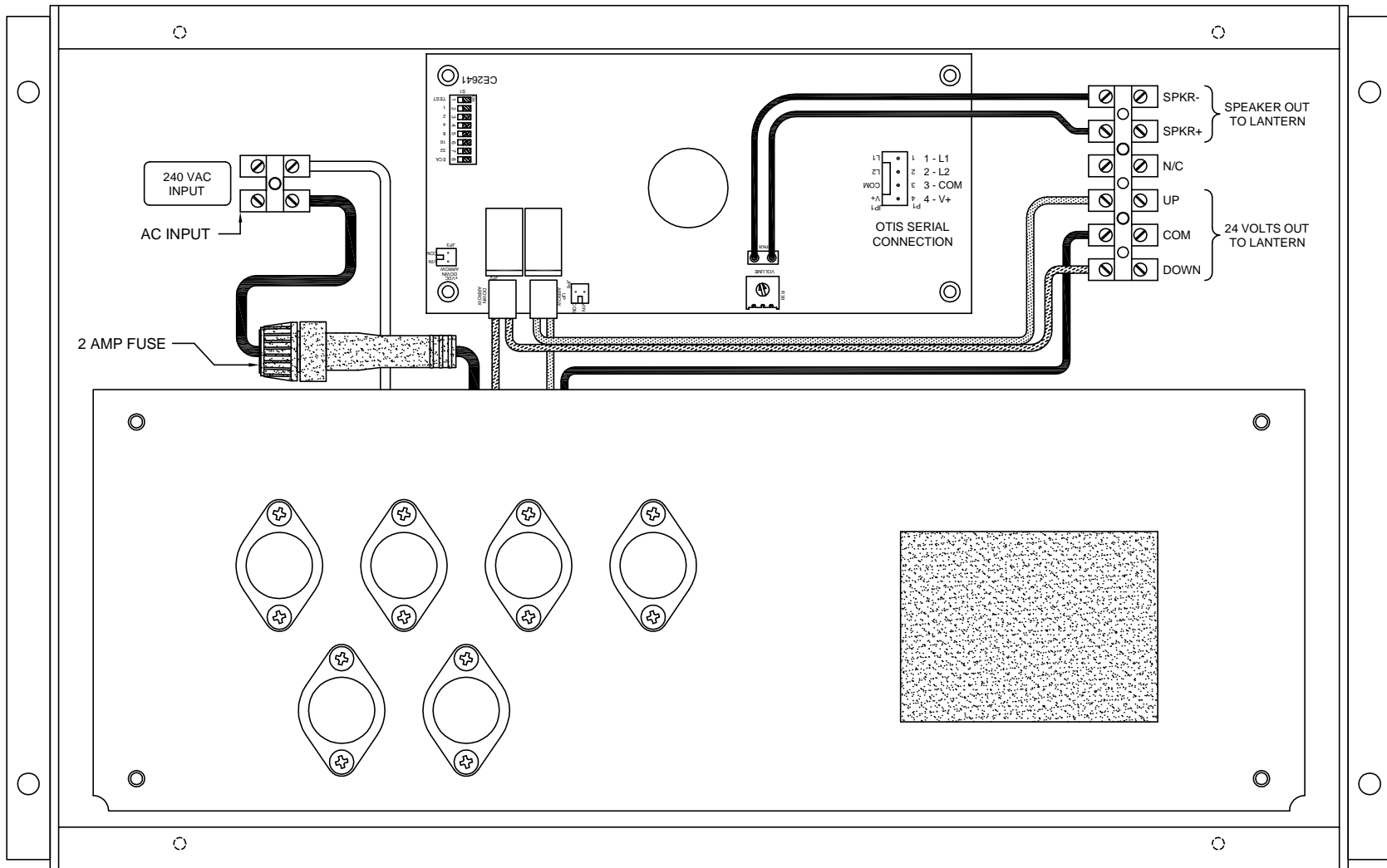
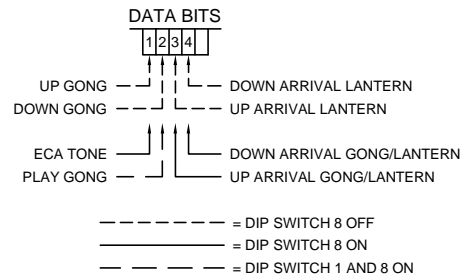


CHART TO SELECT ADDRESS WITH DIP SWITCH

32	16	8	4	2	1	VALUE	32	16	8	4	2	1	VALUE	32	16	8	4	2	1	VALUE
DS7	DS6	DS5	DS4	DS3	DS2	ADDRESS	DS7	DS6	DS5	DS4	DS3	DS2	ADDRESS	DS7	DS6	DS5	DS4	DS3	DS2	ADDRESS
0	0	0	0	0	0	INVALID	0	1	0	1	1	0	ADDR 22	1	0	1	1	0	0	ADDR 44
0	0	0	0	0	1	INVALID	0	1	0	1	1	1	ADDR 23	1	0	1	1	0	1	ADDR 45
0	0	0	0	1	0	INVALID	0	1	1	0	0	0	ADDR 24	1	0	1	1	1	0	ADDR 46
0	0	0	0	1	1	INVALID	0	1	1	0	0	1	ADDR 25	1	0	1	1	1	1	ADDR 47
0	0	0	1	0	0	ADDR 4	0	1	1	0	1	0	ADDR 26	1	1	0	0	0	0	ADDR 48
0	0	0	1	0	1	ADDR 5	0	1	1	0	1	1	ADDR 27	1	1	0	0	0	1	ADDR 49
0	0	0	1	1	0	ADDR 6	0	1	1	1	0	0	ADDR 28	1	1	0	0	1	0	ADDR 50
0	0	0	1	1	1	ADDR 7	0	1	1	1	0	1	ADDR 29	1	1	0	0	1	1	ADDR 51
0	0	1	0	0	0	ADDR 8	0	1	1	1	1	0	ADDR 30	1	1	0	1	0	0	ADDR 52
0	0	1	0	0	1	ADDR 9	0	1	1	1	1	1	ADDR 31	1	1	0	1	0	1	ADDR 53
0	0	1	0	1	0	ADDR 10	1	0	0	0	0	0	ADDR 32	1	1	0	1	1	0	ADDR 54
0	0	1	0	1	1	ADDR 11	1	0	0	0	0	1	ADDR 33	1	1	0	1	1	1	ADDR 55
0	0	1	1	0	0	ADDR 12	1	0	0	0	1	0	ADDR 34	1	1	1	0	0	0	ADDR 56
0	0	1	1	0	1	ADDR 13	1	0	0	0	1	1	ADDR 35	1	1	1	0	0	1	ADDR 57
0	0	1	1	1	0	ADDR 14	1	0	0	1	0	0	ADDR 36	1	1	1	0	1	0	ADDR 58
0	0	1	1	1	1	ADDR 15	1	0	0	1	0	1	ADDR 37	1	1	1	0	1	1	ADDR 59
0	1	0	0	0	0	ADDR 16	1	0	0	1	1	0	ADDR 38	1	1	1	1	0	0	ADDR 60
0	1	0	0	0	1	ADDR 17	1	0	0	1	1	1	ADDR 39	1	1	1	1	0	1	ADDR 61
0	1	0	0	1	0	ADDR 18	1	0	1	0	0	0	ADDR 40	1	1	1	1	1	0	ADDR 62
0	1	0	0	1	1	ADDR 19	1	0	1	0	0	1	ADDR 41	1	1	1	1	1	1	ADDR 63
0	1	0	1	0	0	ADDR 20	1	0	1	0	1	0	ADDR 42							
0	1	0	1	0	1	ADDR 21	1	0	1	0	1	1	ADDR 43							



DIP1	DIP8	FUNCTION
1	0	SELF-TEST MODE
0	0	GONG USES BIT 1 UP (SINGLE) AND BIT 2 DOWN (DOUBLE)
0	1	GONG USES BIT 3 UP AND BIT 4 DOWN (BOTH SINGLE)
1	1	ECA MODE

DATE DRAWN: 10/22/09	DRAWN BY: DAC	REQUESTED BY: RP	 C.E. ELECTRONICS, INC. 2107 Industrial Drive Bryan, Ohio 43306 (419) 636-6705
BOARD NUMBER: 2641	LAST DATE REVISED: -	APPROVED BY:	
PRODUCT: OTIS SERIAL LANTERN DRIVER (RELAY OUTPUT)	DWG. NO. OSRLD-5_01	REV.:	-

The following Otis data must be furnished at the specified address for the Otis Serial Indicator to work properly. The address is selected by setting DIP switches 2-7 as shown on the back of this page. The board reads the address determined by the DIP switch setting. For example, if the DIP switch is set to address 50, the board will read the bits at address 50.

At DIP switch address—selected by the DIP switch on the unit (Default 50):

### **Normal Operation**

DIP switch 1 puts the unit in self-test mode.

DIP switch 1 and DIP switch 8 *OFF*:

- Bit 1—Up Gong (Single)
- Bit 2—Down Gong (Double)
- Bit 3—Up Arrival Arrow/Lantern
- Bit 4—Down Arrival Arrow/Lantern

DIP switch 1 *OFF* and DIP switch 8 *ON*:

- Bit 1—Not Used
- Bit 2—Not Used
- Bit 3—Up Arrival Arrow/Lantern and Up Gong (Single)
- Bit 4—Down Arrival Arrow/Lantern and Down Gong (Single)

### **ECA Operation**

DIP switches 1 and 8 *ON* puts the unit in ECA Mode:

- Bit 1—ECA Tone
- Bit 2—Play Gong
- Bit 3—Up Arrival Arrow/Lantern (Single Gong)
- Bit 4—Down Arrival Arrow/Lantern (Double Gong)