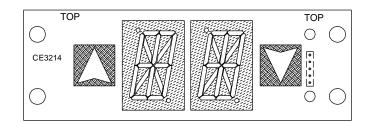
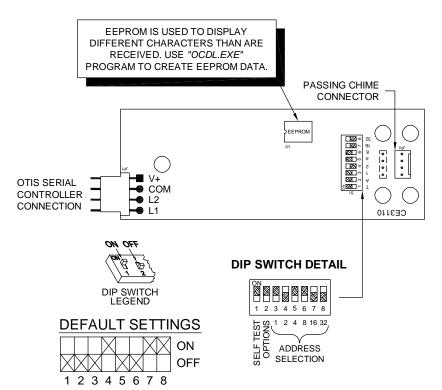
## **OH210-XX**





CODE VERSION

BOARD VERSION CE3110

CE3214 \_\_\_

## CHART TO SELECT ADDRESS WITH DIP SWITCH

32 16 8 4 2 1 — BIT VALUE

DS8	DS7	DS6	DS5	DS4	DS3	ADDRESS							
0	0	0	0	0	0	INVALID	1	0	0	0	0	0	ADDRESS #32
0	0	0	0	0	0	INVALID	1	0	0	0	0	1	ADDRESS #33
0	0	0	0	0	0	INVALID	1	0	0	0	1	0	ADDRESS #34
0	0	0	0	0	0	INVALID	1	0	0	0	1	1	ADDRESS #35
0	0	0	1	0	0	ADDRESS #4	1	0	0	1	0	0	ADDRESS #36
0	0	0	1	0	1	ADDRESS #5	1	0	0	1	0	1	ADDRESS #37
0	0	0	1	1	0	ADDRESS #6	1	0	0	1	1	0	ADDRESS #38
0	0	0	1	1	1	ADDRESS #7	1	0	0	1	1	1	ADDRESS #39
0	0	1	0	0	0	ADDRESS #8	1	0	1	0	0	0	ADDRESS #40
0	0	1	0	0	1	ADDRESS #9	1	0	1	0	0	1	ADDRESS #41
0	0	1	0	1	0	ADDRESS #10	1	0	1	0	1	0	ADDRESS #42
0	0	1	0	1	1	ADDRESS #11	1	0	1	0	1	1	ADDRESS #43
0	0	1	1	0	0	ADDRESS #12	1	0	1	1	0	0	ADDRESS #44
0	0	1	1	0	1	ADDRESS #13	1	0	1	1	0	1	ADDRESS #45
0	0	1	1	1	0	ADDRESS #14	1	0	1	1	1	0	ADDRESS #46
0	0	1	1	1	1	ADDRESS #15	1	0	1	1	1	1	ADDRESS #47
0	1	0	0	0	0	ADDRESS #16	1	1	0	0	0	0	ADDRESS #48
0	1	0	0	0	1	ADDRESS #17	1	1	0	0	0	1	ADDRESS #49
0	1	0	0	1	0	ADDRESS #18	1	1	0	0	1	0	ADDRESS #50
0	1	0	0	1	1	ADDRESS #19	1	1	0	0	1	1	ADDRESS #51
0	1	0	1	0	0	ADDRESS #20	1	1	0	1	0	0	ADDRESS #52
0	1	0	1	0	1	ADDRESS #21	1	1	0	1	0	1	ADDRESS #53
0	1	0	1	1	0	ADDRESS #22	1	1	0	1	1	0	ADDRESS #54
0	1	0	1	1	1	ADDRESS #23	1	1	0	1	1	1	ADDRESS #55
0	1	1	0	0	0	ADDRESS #24	1	1	1	0	0	0	ADDRESS #56
0	1	1	0	0	1	ADDRESS #25	1	1	1	0	0	1	ADDRESS #57
0	1	1	0	1	0	ADDRESS #26	1	1	1	0	1	0	ADDRESS #58
0	1	1	0	1	1	ADDRESS #27	1	1	1	0	1	1	ADDRESS #59
0	1	1	1	0	0	ADDRESS #28	1	1	1	1	0	0	ADDRESS #60
0	1	1	1	0	1	ADDRESS #29	1	1	1	1	0	1	ADDRESS #61
0	1	1	1	1	0	ADDRESS #30	1	1	1	1	1	0	ADDRESS #62
0	1	1	1	1	1	ADDRESS #31	1	1	1	1	1	1	ADDRESS #63

DAT	A BITS
1 2	234
T-	1111
NOT USED —	│
PASSING CHIME (LPT)	UP ARROW
<b>+</b> 1	<b>)</b>
UP ARROW	PASSING CHIME (LPT)
DOWN ARROW	L NOT USED

= DIP SWITCH 2 OFF

---- = DIP SWITCH 2 ON

DIP1	DIP2	FUNCTION
1	Х	SELF-TEST MODE
0	0	ARROW USES BIT 3 (UP) AND BIT 4 (DOWN). CHIME USES BIT 2.
0	1	ARROW USES BIT 1 (UP) AND BIT 2 (DOWN). CHIME USES BIT 4.

DATE DRAWN:	DRAWN BY:	REQUESTED BY:			
07/10/06	DAC	DV	C.E. ELEC	TRONICS, INC.	
BOARD NUMBER:	LAST DATE REVISED:	APPROVED BY:	2107 Industrial Drive Bryan, Ohio 43506 (419) 636-6705		
3110, 3214	-				
PRODUCT			,		
	OH210-XX	DWG. NO.	REV:		
	OTIE TO 701		OH210002	-	

The following Otis data must be furnished at the specified addresses for the Otis Serial Position Indicator to work properly. The address is selected by setting DIP switches 3-8 as shown on the back of this page. The board reads the address determined by the DIP switch setting. For example, if the DIP switch is set to address 50, the board will read the bits at address 50.

At DIP switch address—selected by the DIP switch on the unit (Default 50):

DIP switch 1 puts the unit in self-test mode.

DIP switch 1 and DIP switch 2 OFF:

Bit 1—Not Used

Bit 2—Passing Chime (LPT)

Bit 3—Up Arrow

Bit 4—Down Arrow

DIP switch 1 OFF and DIP switch 2 ON:

Bit 1—Up Arrow

Bit 2—Down Arrow

Bit 3—Not Used

Bit 4—Passing Chime (LPT)

Two position indicators can be used at the same address if the passing chime—LPT is not attached or programmed. Just set DIP switch 2 to opposite values on the two units.

The Otis SPI display can substitute different floor characters for the ones the controller sends, and it can switch between floor characters and ASCII characters for a message. To display alternative floor characters or messages, an EEPROM created using the *OCDL.EXE* program must be installed in the unit. Message characters alternate with floor characters every second.

Messages use the two addresses following the address set by the DIP switch.

DIP switch address +1 (Defa	<u>Priority</u>					
Bit 1—FSL	Fire Service Lamp	1				
Bit 2—Available	Message Two	2				
Bit 3—FNDG/RNDG	Front/Rear Nudging	3				
Bit 4—ISCL	Independent Service	4				
DIP switch address +2 (Default 52):						
Bit 1—OLS	Overload Lamp	5				
Bit 2—PFL	Power Failure Lamp	6				
Bit 3—Available	Message Seven	7				
Bit 4—EQL	Earthquake Lamp	8				

NOTE: If messages are not programmed in the EEPROM, these two addresses are available for other devices, such as other position indicators.