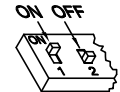
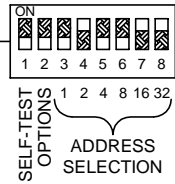


OE205-XX

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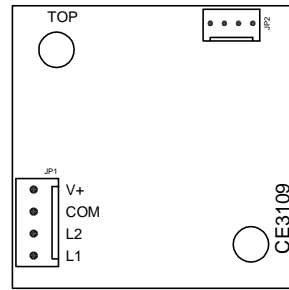
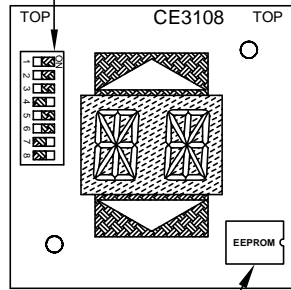
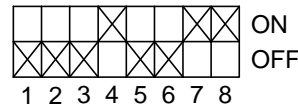
CHART TO SELECT ADDRESS WITH DIP SWITCH

DIP SWITCH DETAIL



DIP SWITCH LEGEND

DEFAULT SETTINGS

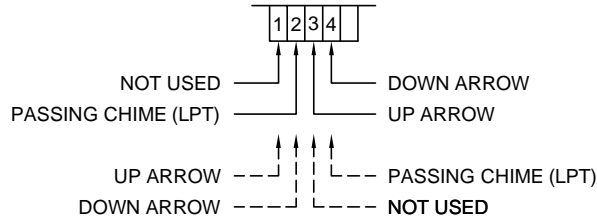


OTIS SERIAL CONTROLLER CONNECTION

EEPROM IS USED TO DISPLAY DIFFERENT CHARACTERS THAN ARE RECEIVED. USE "OCDL.EXE" PROGRAM TO CREATE EEPROM DATA.

DIP SWITCH #	8	7	6	5	4	3	ADDRESS
VALUE	32	16	8	4	2	1	
0	0	0	0	0	0	0	INVALID
0	0	0	0	0	0	1	ADDRESS #32
0	0	0	0	0	1	0	ADDRESS #33
0	0	0	0	0	1	1	ADDRESS #34
0	0	0	0	1	0	0	ADDRESS #35
0	0	0	1	0	0	0	ADDRESS #36
0	0	1	0	0	0	0	ADDRESS #37
0	0	1	1	0	0	0	ADDRESS #38
0	0	1	1	1	0	0	ADDRESS #39
0	0	1	0	0	0	0	ADDRESS #40
0	0	1	0	0	1	0	ADDRESS #41
0	0	1	0	1	0	0	ADDRESS #42
0	0	1	0	1	1	0	ADDRESS #43
0	0	1	1	0	0	0	ADDRESS #44
0	0	1	1	0	1	0	ADDRESS #45
0	0	1	1	1	0	0	ADDRESS #46
0	0	1	1	1	1	0	ADDRESS #47
0	1	0	0	0	0	0	ADDRESS #48
0	1	0	0	0	1	0	ADDRESS #49
0	1	0	0	1	0	0	ADDRESS #50
0	1	0	0	1	1	0	ADDRESS #51
0	1	0	1	0	0	0	ADDRESS #52
0	1	0	1	0	1	0	ADDRESS #53
0	1	0	1	1	0	0	ADDRESS #54
0	1	0	1	1	1	0	ADDRESS #55
0	1	1	0	0	0	0	ADDRESS #56
0	1	1	0	0	1	0	ADDRESS #57
0	1	1	0	1	0	0	ADDRESS #58
0	1	1	0	1	1	0	ADDRESS #59
0	1	1	1	0	0	0	ADDRESS #60
0	1	1	1	0	1	0	ADDRESS #61
0	1	1	1	1	0	0	ADDRESS #62
0	1	1	1	1	1	0	ADDRESS #63

DATA BITS



DIP1	DIP2	FUNCTION
1	X	SELF-TEST MODE
0	0	ARROW USES BIT 3 (UP) AND BIT 4 (DOWN). CHIME USES BIT 2.
0	1	ARROW USES BIT 1 (UP) AND BIT 2 (DOWN). CHIME USES BIT 4.

CODE VERSION _____

BOARD VERSION CE3108 _____

CE3109 _____

_____ = DIP SWITCH 2 OFF
 ----- = DIP SWITCH 2 ON

The following Otis data must be furnished at the specified addresses for the Otis Serial Position Indicator to work properly. The address is selected by setting DIP switches 3-8 as shown on the back of this page. The board reads the address determined by the DIP switch setting. For example, if the DIP switch is set to address 50, the board will read the bits at address 50.

At DIP switch address—selected by the DIP switch on the unit (Default 50):

DIP switch 1 puts the unit in self-test mode.

DIP switch 1 and DIP switch 2 *OFF*:

- Bit 1—Not Used
- Bit 2—Passing Chime (LPT)
- Bit 3—Up Arrow
- Bit 4—Down Arrow

DIP switch 1 *OFF* and DIP switch 2 *ON*:

- Bit 1—Up Arrow
- Bit 2—Down Arrow
- Bit 3—Not Used
- Bit 4—Passing Chime (LPT)

Two position indicators can be used at the same address if the passing chime—LPT is not attached or programmed. Just set DIP switch 2 to opposite values on the two units.

The Otis SPI display can substitute different floor characters for the ones the controller sends, and it can switch between floor characters and ASCII characters for a message. To display alternative floor characters or messages, an EEPROM created using the *OCDL.EXE* program must be installed in the unit. Message characters alternate with floor characters every second.

Messages use the two addresses following the address set by the DIP switch.

DIP switch address +1 (Default 51):		<u>Priority</u>
Bit 1—450	Fire	1
Bit 2—FSL/RFSL	Fire Service Lamp	2
Bit 3—FNDG/RNDG	Front/Rear Nudging	3
Bit 4—ISCL	Independent Service	4
DIP switch address +2 (Default 52):		
Bit 1—OLS	Overload Lamp	5
Bit 2—Available	Message Six	6
Bit 3—Available	Message Seven	7
Bit 4—Available	Message Eight	8

NOTE: If messages are not programmed in the EEPROM, these two addresses are available for other devices, such as other position indicators.