

GTDV-OS

JOB# _____

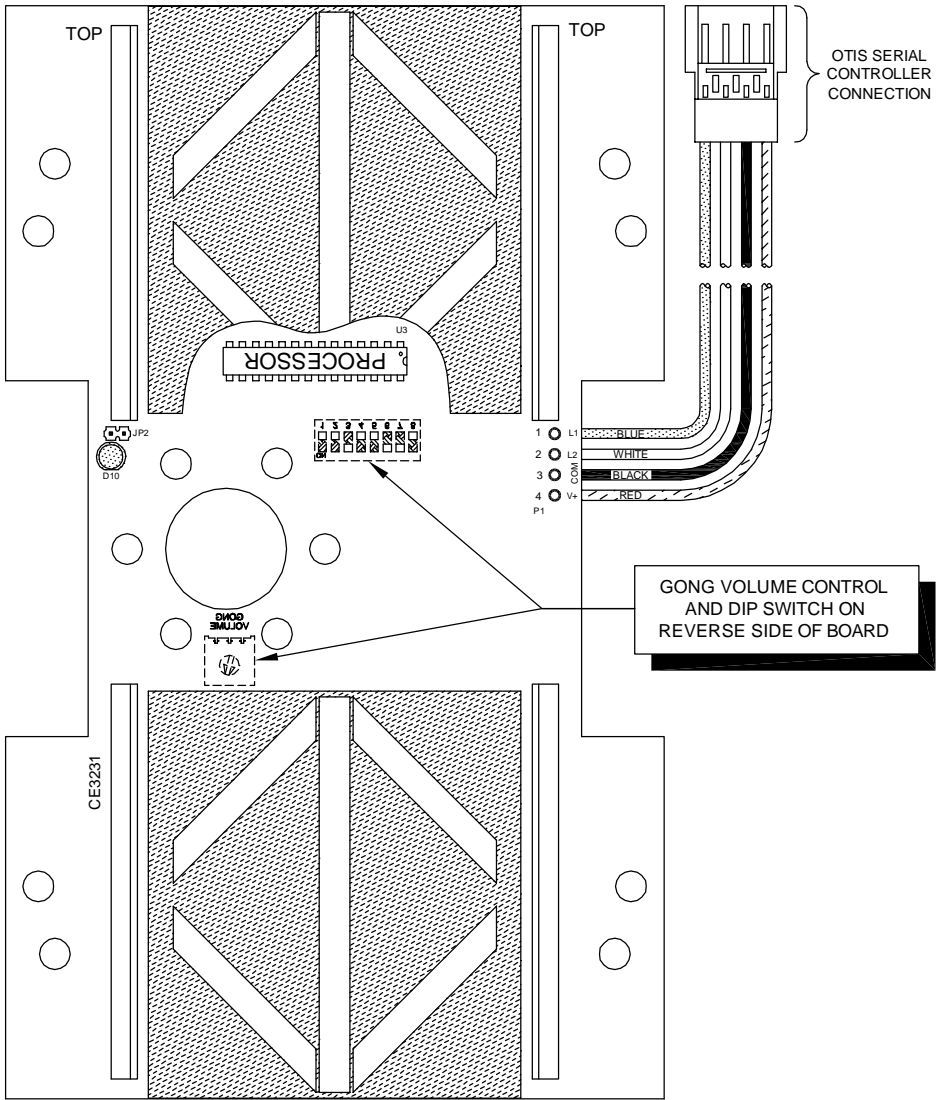
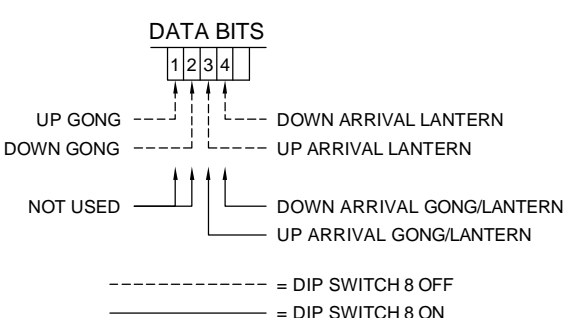
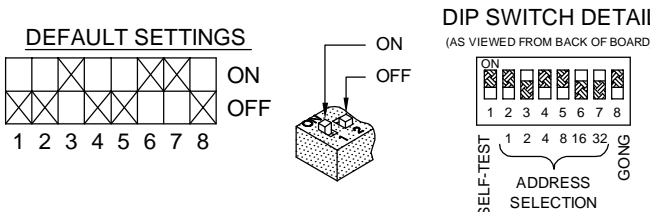


CHART TO SELECT ADDRESS WITH DIP SWITCH

32	16	8	4	2	1	VALUE
DS7	DS6	DS5	DS4	DS3	DS2	ADDRESS
0	0	0	0	0	0	INVALID
0	0	0	0	0	0	INVALID
0	0	0	0	0	0	INVALID
0	0	0	0	0	0	INVALID
0	0	0	1	0	0	ADDRESS #4
0	0	0	1	0	1	ADDRESS #5
0	0	0	1	1	0	ADDRESS #6
0	0	0	1	1	1	ADDRESS #7
0	0	1	0	0	0	ADDRESS #8
0	0	1	0	0	1	ADDRESS #9
0	0	1	0	1	0	ADDRESS #10
0	0	1	0	1	1	ADDRESS #11
0	0	1	1	0	0	ADDRESS #12
0	0	1	1	0	1	ADDRESS #13
0	0	1	1	1	0	ADDRESS #14
0	0	1	1	1	1	ADDRESS #15
0	1	0	0	0	0	ADDRESS #16
0	1	0	0	0	1	ADDRESS #17
0	1	0	0	1	0	ADDRESS #18
0	1	0	0	1	1	ADDRESS #19
0	1	0	1	0	0	ADDRESS #20
0	1	0	1	0	1	ADDRESS #21
0	1	0	1	1	0	ADDRESS #22
0	1	0	1	1	1	ADDRESS #23
0	1	1	0	0	0	ADDRESS #24
0	1	1	0	0	1	ADDRESS #25
0	1	1	0	1	0	ADDRESS #26
0	1	1	0	1	1	ADDRESS #27
0	1	1	1	0	0	ADDRESS #28
0	1	1	1	0	1	ADDRESS #29
0	1	1	1	1	0	ADDRESS #30
0	1	1	1	1	1	ADDRESS #31
1	0	0	0	0	0	ADDRESS #32
1	0	0	0	0	1	ADDRESS #33
1	0	0	0	1	0	ADDRESS #34
1	0	0	0	1	1	ADDRESS #35
1	0	0	1	0	0	ADDRESS #36
1	0	0	1	0	1	ADDRESS #37
1	0	0	1	1	0	ADDRESS #38
1	0	0	1	1	1	ADDRESS #39
1	0	1	0	0	0	ADDRESS #40
1	0	1	0	0	1	ADDRESS #41
1	0	1	0	1	0	ADDRESS #42
1	0	1	0	1	1	ADDRESS #43
1	0	1	1	0	0	ADDRESS #44
1	0	1	1	0	1	ADDRESS #45
1	0	1	1	1	0	ADDRESS #46
1	0	1	1	1	1	ADDRESS #47
1	1	0	0	0	0	ADDRESS #48
1	1	0	0	0	1	ADDRESS #49
1	1	0	0	1	0	ADDRESS #50
1	1	0	0	1	1	ADDRESS #51
1	1	0	1	0	0	ADDRESS #52
1	1	0	1	0	1	ADDRESS #53
1	1	0	1	1	0	ADDRESS #54
1	1	0	1	1	1	ADDRESS #55
1	1	1	0	0	0	ADDRESS #56
1	1	1	0	0	1	ADDRESS #57
1	1	1	0	1	0	ADDRESS #58
1	1	1	0	1	1	ADDRESS #59
1	1	1	1	0	0	ADDRESS #60
1	1	1	1	0	1	ADDRESS #61
1	1	1	1	1	0	ADDRESS #62
1	1	1	1	1	1	ADDRESS #63



CODE VERSION _____
 BOARD VERSION CE3231 _____

DIP1	DIP8	FUNCTION
1	X	SELF-TEST MODE
0	0	GONG USES BIT 1 UP (SINGLE) AND BIT 2 DOWN (DOUBLE)
0	1	GONG USES BIT 3 UP AND BIT 4 DOWN (BOTH SINGLE)

The following Otis data must be furnished at the specified addresses for the Otis Serial Lantern to work properly. The address is selected by setting DIP switches 2-7 as shown on the back of this page. The board reads the address determined by the DIP switch setting. For example, if the DIP switch is set to address 50, the board will read the bits at address 50.

At DIP switch address - selected by the DIP switch setting on the unit (Default 50):

DIP switch 1 puts the unit in self-test mode.

DIP switch 1 and DIP switch 8 *OFF*:

- Bit 1 - Up Gong (Single)
- Bit 2 - Down Gong (Double)
- Bit 3 - Up Arrival Arrow/Lantern
- Bit 4 - Down Arrival Arrow/Lantern

DIP switch 1 *OFF* and DIP switch 8 *ON*:

- Bit 1 - Not Used
- Bit 2 - Not Used
- Bit 3 - Up Arrival Arrow/Lantern and Up Gong (Single)
- Bit 4 - Down Arrival Arrow/Lantern and Down Gong (Single)

NOTE: ECA function is not available with this unit.