

The following Otis data must be furnished at the specified addresses for the Otis Serial Indicator to work properly. The address is selected by setting DIP switches 2-7 as shown on the back of this page. The board reads the address determined by the DIP switch setting. For example, if the DIP switch is set to address 50, the board will read the bits at address 50.

At DIP switch address—selected by the DIP switch on the unit (Default 50):

Normal Operation

DIP switch 1 puts the unit in self-test mode.

DIP switch 1 and DIP switch 8 *OFF*:

- Bit 1—Up Gong (Single)
- Bit 2—Down Gong (Double)
- Bit 3—Up Arrival Arrow/Lantern
- Bit 4—Down Arrival Arrow/Lantern

DIP switch 1 *OFF* and DIP switch 8 *ON*:

- Bit 1—Not Used
- Bit 2—Not Used
- Bit 3—Up Arrival Arrow/Lantern and Up Gong (Single)
- Bit 4—Down Arrival Arrow/Lantern and Down Gong (Single)

ECA Operation

DIP switches 1 and 8 *ON* puts the unit in ECA Mode:

- Bit 1—ECA Tone
- Bit 2—Play Gong
- Bit 3—Up Arrival Arrow/Lantern (Single Gong)
- Bit 4—Down Arrival Arrow/Lantern (Double Gong)

The unit display can substitute different floor characters for the ones the controller sends, and it can switch between floor characters and ASCII characters for a message. To display alternative floor characters or messages, an EEPROM created using the *OCDL.EXE* program must be installed in the unit. Message characters alternate with floor characters every second.

Messages use the two addresses following the address set by the DIP switch.

DIP switch address +1 (Default 51): Priority

- | | | |
|-----------------|---------------------|---|
| Bit 1—FSL/RFSL | Fire Service Lamp | 1 |
| Bit 2—Available | Message Two | 2 |
| Bit 3—FNDG/RNDG | Front/Rear Nudging | 3 |
| Bit 4—ISCL | Independent Service | 4 |

DIP switch address +2 (Default 52):

- | | | |
|------------------|---------------|---|
| Bit 1—OLS | Overload Lamp | 5 |
| Bit 2— Available | Message Six | 6 |
| Bit 3— Available | Message Seven | 7 |
| Bit 4— Available | Message Eight | 8 |

NOTE: If messages are not programmed in the EEPROM, these two addresses are available for other devices, such as other position indicators.